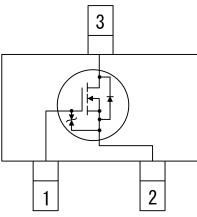
#### N-channel MOSFET 60V, 0.15A

#### ■FEATURES

On-State Resistance Driving voltage Environmentally Friendly

- : R<sub>DS</sub>(on)=5Ω @V<sub>GS</sub> =10V : 4.5V
- : EU RoHS Compliant, Pb Free

### EQUIVALENT CIRCUIT

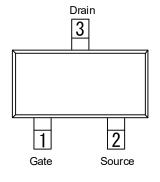




■ PIN CONFIGURATION

ETR11057-001a

●SOT-323-3A



#### ■ PRODUCT NAME

PRODUCT NAME	PACKAGE	ORDER UNIT	
XP261N70023R-G *	SOT-323-3A	3,000pcs/ Reel	

\* The "-G" suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant

#### ■ABSOLUTE MAXIMUM RATINGS

			Ta=25℃
PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V <sub>DSS</sub>	60	V
Gate-Source Voltage	V <sub>GSS</sub>	±20	V
Drain Current (DC)	ID	0.15	А
Drain Current(Pulse) (*1)	I <sub>DP</sub>	0.3	A
Channel Power Dissipation (*2)	Pd	0.35	W
Junction Temperature	TJ	150	С°
Storage Temperature	T <sub>stg</sub>	-55~150	С°

 $^{*(1)}$  PW  ${\leq}\,10\mu s,duty\,cycle {\leq}\,1\%$ 

 $^{*(*2)}$ When implemented on a PCB defined by JESD51-7

#### ■ELECTRICAL CHARACTERISTICS

						Ta=25℃
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 250µA, V <sub>GS</sub> = 0V	60	-	-	V
Drain-Source Leakage Current	IDSS	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	-	-	1	μA
Gate-Source Leakage Current	lgss	$V_{GS}$ = ±20V, $V_{DS}$ = 0V	-	-	±10	μA
Gate Threshold Voltage	V <sub>GS(off)</sub>	$I_D$ = 250uA, $V_{DS}$ = $V_{GS}$	0.9	1.5	2.1	V
Drain-Source On Resistance	<b>D</b>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 100mA	-	3	5	Ω
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 100mA	-	3.5	5.5	Ω
Input Capacitance	Ciss		-	18	-	pF
Output Capacitance	Coss	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V f=1MHz	-	4.5	-	pF
Reverse Transfer Capacitance	Crss		-	1.5	-	pF
Turn-on Delay Time	t <sub>d(on)</sub>		-	9	-	ns
Rise Time	tr	V <sub>DD</sub> = 10V, I <sub>D</sub> = 100mA	-	4	-	ns
Turn-off Delay Time	t <sub>d(off)</sub>	V <sub>GS</sub> = 10V	-	30	-	ns
Fall Time	tr		-	9	-	ns
Total Gate Charge	Qg	<u> </u>	-	0.38	-	nC
Gate-Source Charge	Qgs	V <sub>DS</sub> = 20V, I <sub>D</sub> = 100mA V <sub>GS</sub> = 10V	-	0.06	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	0.16	-	nC
Diode Forward Voltage	Vsd	Is= 100mA, V <sub>GS</sub> = 0V	-	0.8	1.2	V

#### ■NOTES ON USE

1. Please use this IC within the absolute maximum ratings.

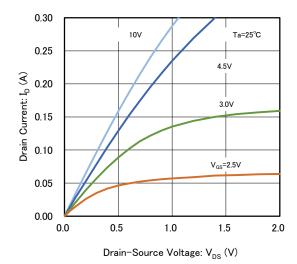
Even within the ratings, in case of high load use continuously such as high temperature, high voltage, high current and thermal stress may cause reliability degradation of the IC.

2. Torex places an importance on improving our products and their reliability.

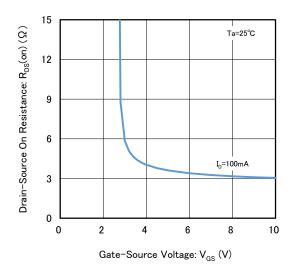
We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.

#### ■TYPICAL PERFORMANCE CHARACTERISTICS

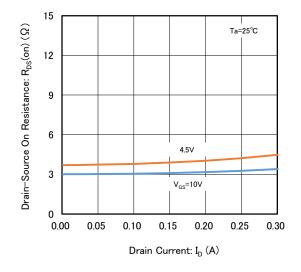
(1) Drain Current vs. Drain-Source Voltage



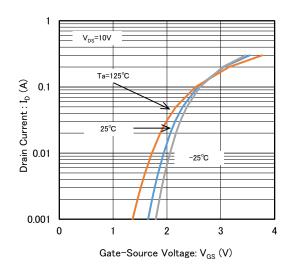
(3) Drain-Source On Resistance vs. Gate-Source Voltage



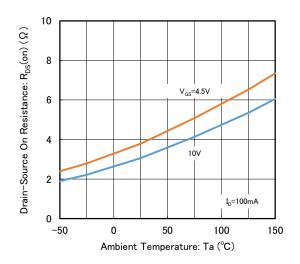
(5) Drain-Source On Resistance vs. Drain Current



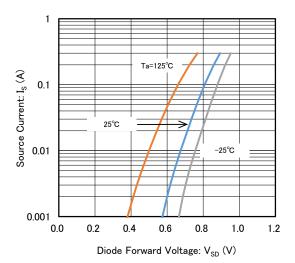
(2) Drain Current vs. Gate-Source Voltage



(4) Drain-Source On Resistance vs. Ambient Temperature



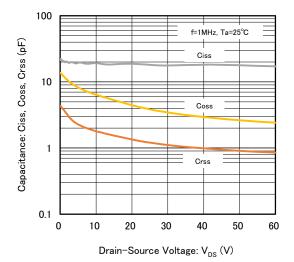
(6) Source Current vs. Diode Forward Voltage



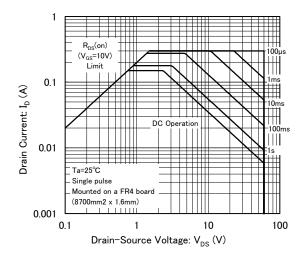
#### ■TYPICAL PERFORMANCE CHARACTERISTICS

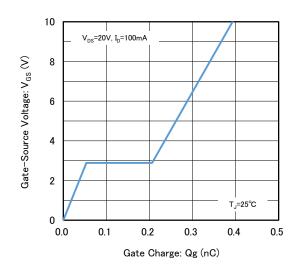
(7) Ciss, Coss, Crss vs. Drain-Source Voltage

(8) Gate-Source Voltage vs. Gate Charge



#### (9) Area of Safe Operation





#### ■PACKAGING INFORMATION

For the latest package information go to, www.torexsemi.com/technical-support/packages

PACKAGE	OUTLINE / LAND PATTERN	THERMAL CHARACTERISTICS	
SOT-323-3A	<u>SOT-323-3A PKG</u>	JESD51-7 Board	SOT-323-3A Power Dissipation

#### ■MARKING RULE

●SOT-323-3A

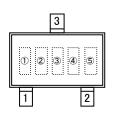
1,2,3 represents product series

	MARK		PRODUCT SERIES
1	2	3	PRODUCT SERIES
6	1	Ν	XP261N7002**-G

(4),(5) represents production lot number.

01 to 09, 0A to 0Z, 11 to 9Z, A1 to A9, AA to AZ, B1 to ZZ repeated (G, I, J, O, Q, W excluded) \*No character inversion used

SOT-323-3A



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