XC9519 Series

Dual Output Step-Up/Inverting DC/DC Converter

■ GENERAL DESCRIPTION

The XC9519 series is a 2 channel (step-up and inverting) DC/DC converter IC. One DC/DC converter is a step-up DC/DC and the other is an inverting DC/DC converter. The step-up converter compares a built-in reference voltage 1.0V to the FBP voltage (accuracy ±1.5%) and a positive output voltage can be set freely with the external components up to 18V. The inverting DC/DC converter compares a difference between a reference voltage and the FBN voltage (accuracy ±1.5%) to the GND, then a negative output voltage can be set until -15V with the external components.

With a 1.2MHz frequency, the size of the external components can be reduced. As for operation mode, the device can be selected to use PWM control or automatic PWM/PFM switching control by the MODE pin. In the automatic PWM/PFM switching control mode, control switches from PWM to PFM during light loads. The series is highly efficient from light loads through to large output currents. In the PWM control mode, noise is easily reduced since the frequency is fixed. The control mode can be selected for each application. The soft start and current control functions are internally optimized. During stand-by, all circuits in the IC are shutdown to reduce current consumption to as low as 1.0μA or less. The device includes a gate control pin for the P-channel MOSFET which is used for a load disconnection at the stand-by mode. The GAINP and GAINN pins are used for loop compensation in order to optimize load transient response. With the built-in UVLO (Under Voltage Lock Out) function, the internal driver transistor is forced OFF when input voltage becomes 2.2V or lower.

■ FEATURES

- Input Voltage : 2.7V ~ 5.5V
- Output Current : 500mA @VIN=3.7V, VOUTP=5.0V, VOUTN=-5.0V
- Positive Output Voltage : 4.0V (*1)
- Negative Output Voltage : -15.0V (*2) ~ -4.0V (accuracy ±1.5% @25°C)
- Oscillation Frequency : 1.2MHz
- Soft-Start Circuit Built-In
- Positive : 2.5ms (TYP.)
- Inverting : 2.2ms (TYP.)
- Protection Circuits : Over Current Limit (Integral Latching)
- Short Protection Latching
- UVLO
- Thermal Shutdown
- Over Voltage Protection
- Function Addition : Control Pin
- Load disconnect Pin
- Phase Compensation Pin
- Ceramic Capacitor Compatible
- Operating Ambient Temperature : -40°C ~ +85°C
- Package : QFN-24
- Environmentally Friendly : EU RoHS Compliant, Pb Free

(1) VOUTPSET=VIN + 0.2V (VOUTPSET : Positive output voltage range)
(2) VIN - VOUTNSET + VFN <= 21.0V
(VFN : Forward voltage of SBDN, VOUTNSET : Negative output voltage range)

■ TYPICAL PERFORMANCE CHARACTERISTICS

\[ V_{OUTP}=5.0V, V_{OUTN}=-5.0V, I_{OUTP}=I_{OUTN} \]

\[ C_{D}, C_{D}=4\times4.7\mu F, L_{P}=3.3\mu H, V_{IN}=5V, R_{SP}=7.5k\Omega, C_{ZP}=4.7nF, R_{ZP}=130k\Omega, C_{ZN}=0.47nF \]

P-ch MOS: EMH1303, RSP=7.5KΩ, COUT=4.7nF, RSP=130KΩ, COUT=0.47nF

PWM/PFM (VMODE="H")
PWM (VMODE="L")

■ APPLICATIONS

- AMOLED
  - Smartphones
  - Tablet PCs
  - Automotive navigation systems
- CCD image sensors
  - Surveillance cameras
- e-paper
  - e-Books

■ TYPICAL APPLICATION CIRCUIT

[Diagram of the XC9519 series application circuit]

[Graph showing efficiency and output current characteristics]
**PRODUCT CLASSIFICATION**

- **Ordering Information**
  XC9519①②③④⑤⑥⑦

<table>
<thead>
<tr>
<th>DESIGNATOR</th>
<th>ITEM</th>
<th>SYMBOL</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>①</td>
<td>UVLO Detect Voltage</td>
<td>A</td>
<td>UVLO Detect Voltage 2.2V</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>UVLO Hysteresis width 0.2V</td>
</tr>
<tr>
<td>②③</td>
<td>Oscillation Frequency</td>
<td>12</td>
<td>1.2 MHz</td>
</tr>
<tr>
<td>④</td>
<td>Maximum Current Limit</td>
<td>A</td>
<td>2.0A</td>
</tr>
<tr>
<td>⑤⑥⑦⑧</td>
<td>Package (Order Unit)</td>
<td>ZR-G</td>
<td>QFN-24 (1,000/Reel) (②)</td>
</tr>
</tbody>
</table>

(①) The “-G” suffix denotes Halogen and Antimony free as well as being fully EU RoHS compliant.
(②) The XC9519 reels are shipped in a moisture-proof packing.

* Diodes inside the circuit are an ESD protection diode and a parasitic diode.
**PIN CONFIGURATION**

*1: The back metal pad, AGND pin and two PGND pins (No. 21 and 22) should be connected outside.

**PIN ASSIGNMENT**

<table>
<thead>
<tr>
<th>PIN NUMBER</th>
<th>PIN NAME</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>1, 2</td>
<td>PVIN</td>
<td>Power Supply Input 1</td>
</tr>
<tr>
<td>3</td>
<td>NC</td>
<td>No Connection</td>
</tr>
<tr>
<td>4, 5</td>
<td>LXN</td>
<td>Switching of Inverting DC/DC Converter</td>
</tr>
<tr>
<td>6</td>
<td>VOUTN</td>
<td>Detect Monitoring of Inverting DC/DC Output Voltage</td>
</tr>
<tr>
<td>7</td>
<td>MODE</td>
<td>Selection Pin for Control Mode</td>
</tr>
<tr>
<td>8</td>
<td>VREF</td>
<td>Reference Output Voltage</td>
</tr>
<tr>
<td>9</td>
<td>AVIN</td>
<td>Power Supply Input 2</td>
</tr>
<tr>
<td>10</td>
<td>FBN</td>
<td>Feedback Pin for Inverting DC/DC Converter</td>
</tr>
<tr>
<td>11</td>
<td>GAINN</td>
<td>Loop Compensation Pin for Inverting DC/DC Converter</td>
</tr>
<tr>
<td>12</td>
<td>AGND</td>
<td>Analog Ground</td>
</tr>
<tr>
<td>13</td>
<td>GAINP</td>
<td>Loop Compensation Pin for Step-Up DC/DC Converter</td>
</tr>
<tr>
<td>14</td>
<td>FBP</td>
<td>Feedback Pin for Step-Up DC/DC Converter</td>
</tr>
<tr>
<td>15</td>
<td>ENP</td>
<td>Chip Enable Pin for Step-Up DC/DC Converter</td>
</tr>
<tr>
<td>16</td>
<td>BSW</td>
<td>P-channel MOS FET Gate Control Pin</td>
</tr>
<tr>
<td>17</td>
<td>VOUTP</td>
<td>Output Voltage Sense for Step-Up DC/DC Converter</td>
</tr>
<tr>
<td>18, 19</td>
<td>LXP</td>
<td>Switching Output of Step-Up DC/DC Converter</td>
</tr>
<tr>
<td>20</td>
<td>NC</td>
<td>No Connection</td>
</tr>
<tr>
<td>21, 22</td>
<td>PGND</td>
<td>Power Ground</td>
</tr>
<tr>
<td>23</td>
<td>ENN</td>
<td>Chip Enable Pin for Inverting DC/DC Converter</td>
</tr>
<tr>
<td>24</td>
<td>SWP</td>
<td>Detect Monitoring Voltage Pin for P-channel MOS FET Drain</td>
</tr>
</tbody>
</table>
1. ENP Pin Function

<table>
<thead>
<tr>
<th>ENP PIN</th>
<th>STATUS</th>
</tr>
</thead>
<tbody>
<tr>
<td>H</td>
<td>Step-up DC/DC Converter Active</td>
</tr>
<tr>
<td>L</td>
<td>Step-up DC/DC Converter Stand-by</td>
</tr>
</tbody>
</table>

* Please do not leave the ENP pin open.

2. ENN Pin Function

<table>
<thead>
<tr>
<th>ENN PIN</th>
<th>STATUS</th>
</tr>
</thead>
<tbody>
<tr>
<td>H</td>
<td>Inverting DC/DC Converter Active</td>
</tr>
<tr>
<td>L</td>
<td>Inverting DC/DC Converter Stand-by</td>
</tr>
</tbody>
</table>

* Please do not leave the ENP pin open.

3. MODE Pin Function

<table>
<thead>
<tr>
<th>MODE PIN</th>
<th>STATUS</th>
</tr>
</thead>
<tbody>
<tr>
<td>H</td>
<td>Auto PWM/PFM</td>
</tr>
<tr>
<td>L</td>
<td>PWM Control</td>
</tr>
</tbody>
</table>

* Please do not leave the MODE pin open.
### ABSOLUTE MAXIMUM RATINGS

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SYMBOL</th>
<th>RATINGS</th>
<th>UNITS</th>
</tr>
</thead>
<tbody>
<tr>
<td>PVIN Pin Voltage</td>
<td>V&lt;sub&gt;PVIN&lt;/sub&gt;</td>
<td>-0.3 ~ +6.0</td>
<td>V</td>
</tr>
<tr>
<td>AVIN Pin Voltage</td>
<td>V&lt;sub&gt;AVIN&lt;/sub&gt;</td>
<td>-0.3 ~ +6.0</td>
<td>V</td>
</tr>
<tr>
<td>ENP Pin Voltage</td>
<td>V&lt;sub&gt;ENP&lt;/sub&gt;</td>
<td>-0.3 ~ +6.0</td>
<td>V</td>
</tr>
<tr>
<td>ENN Pin Voltage</td>
<td>V&lt;sub&gt;ENN&lt;/sub&gt;</td>
<td>-0.3 ~ +6.0</td>
<td>V</td>
</tr>
<tr>
<td>MODE Pin Voltage</td>
<td>V&lt;sub&gt;MODE&lt;/sub&gt;</td>
<td>-0.3 ~ +6.0</td>
<td>V</td>
</tr>
<tr>
<td>LXP Pin Voltage</td>
<td>V&lt;sub&gt;LXP&lt;/sub&gt;</td>
<td>-0.3 ~ +22.0</td>
<td>V</td>
</tr>
<tr>
<td>LXN Pin Voltage</td>
<td>V&lt;sub&gt;LXN&lt;/sub&gt;</td>
<td>V&lt;sub&gt;PVIN&lt;/sub&gt; -22.0 ~ V&lt;sub&gt;PVIN&lt;/sub&gt; +0.3</td>
<td>V</td>
</tr>
<tr>
<td>FBP Pin Voltage</td>
<td>V&lt;sub&gt;FBP&lt;/sub&gt;</td>
<td>-0.3 ~ +6.0</td>
<td>V</td>
</tr>
<tr>
<td>FBN Pin Voltage</td>
<td>V&lt;sub&gt;FBN&lt;/sub&gt;</td>
<td>-0.3 ~ +6.0</td>
<td>V</td>
</tr>
<tr>
<td>VOUTP Pin Voltage</td>
<td>V&lt;sub&gt;OUTP&lt;/sub&gt;</td>
<td>-0.3 ~ +22.0</td>
<td>V</td>
</tr>
<tr>
<td>VOUTN Pin Voltage</td>
<td>V&lt;sub&gt;OUTN&lt;/sub&gt;</td>
<td>V&lt;sub&gt;AVIN&lt;/sub&gt; -22.0 ~ V&lt;sub&gt;AVIN&lt;/sub&gt; +0.3</td>
<td>V</td>
</tr>
<tr>
<td>BSW Pin Voltage</td>
<td>V&lt;sub&gt;BSW&lt;/sub&gt;</td>
<td>-0.3 ~ +6.0</td>
<td>V</td>
</tr>
<tr>
<td>SWP Pin Voltage</td>
<td>V&lt;sub&gt;SWP&lt;/sub&gt;</td>
<td>-0.3 ~ +6.0</td>
<td>V</td>
</tr>
<tr>
<td>VREF Pin Voltage</td>
<td>V&lt;sub&gt;REF&lt;/sub&gt;</td>
<td>-0.3 ~ +6.0</td>
<td>V</td>
</tr>
<tr>
<td>GAINP Pin Voltage</td>
<td>V&lt;sub&gt;GAINP&lt;/sub&gt;</td>
<td>-0.3 ~ +6.0</td>
<td>V</td>
</tr>
<tr>
<td>GAINN Pin Voltage</td>
<td>V&lt;sub&gt;GAINN&lt;/sub&gt;</td>
<td>-0.3 ~ +6.0</td>
<td>V</td>
</tr>
<tr>
<td>LXP Pin Current</td>
<td>I&lt;sub&gt;LXP&lt;/sub&gt;</td>
<td>4000 mA</td>
<td>mA</td>
</tr>
<tr>
<td>LXN Pin Current</td>
<td>I&lt;sub&gt;LXN&lt;/sub&gt;</td>
<td>4000 mA</td>
<td>mA</td>
</tr>
<tr>
<td>Power Dissipation</td>
<td>Pd</td>
<td>1500 (PCB mounted) *</td>
<td>mW</td>
</tr>
<tr>
<td>Operating Ambient Temperature</td>
<td>T&lt;sub&gt;opr&lt;/sub&gt;</td>
<td>-40 ~ +85</td>
<td>°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>T&lt;sub&gt;stg&lt;/sub&gt;</td>
<td>-55 ~ +125</td>
<td>°C</td>
</tr>
</tbody>
</table>

* All voltages are described based on the AGND and PGND pin.
* The value is an example of data which is taken with the PCB mounted. Please refer to our web site for details.
### ELECTRICAL CHARACTERISTICS

#### XC9519 Series, Common Characteristics

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SYMBOL</th>
<th>CONDITIONS (1)</th>
<th>MIN.</th>
<th>TYP.</th>
<th>MAX.</th>
<th>UNITS</th>
<th>CIRCUIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Voltage</td>
<td>$V_{IN}$</td>
<td>$V_{\text{ENP}} = 1.5,\text{V}$, $V_{\text{ENN}} = V_{\text{FBP}} = 0,\text{V}$, $V_{\text{FBN}} = 0.1,\text{V}$ $V_{\text{IN}}$ is decreasing from 2.4V.</td>
<td>2.0</td>
<td>2.2</td>
<td>2.4</td>
<td>V</td>
<td>9</td>
</tr>
<tr>
<td>UVLO Detect Voltage</td>
<td>$V_{\text{UVLO}}$</td>
<td>$V_{\text{ENP}} = 1.5,\text{V}$, $V_{\text{ENN}} = V_{\text{FBP}} = 0,\text{V}$, $V_{\text{FBN}} = 0.1,\text{V}$ $V_{\text{IN}}$ is increasing from $V_{\text{UVLO}}$.</td>
<td>2.2</td>
<td>2.4</td>
<td>2.6</td>
<td>V</td>
<td>9</td>
</tr>
<tr>
<td>UVLO Release Voltage</td>
<td>$V_{\text{UVLOR}}$</td>
<td>$V_{\text{IN}} = 2.7,\text{V}$ - $5.5,\text{V}$</td>
<td>2.0</td>
<td>2.2</td>
<td>2.4</td>
<td>V</td>
<td>9</td>
</tr>
<tr>
<td>UVLO Hysteresis Range</td>
<td>$V_{\text{UVLOH}}$</td>
<td>$V_{\text{IN}} = 2.7,\text{V}$ - $5.5,\text{V}$</td>
<td>1.4</td>
<td>-</td>
<td>-</td>
<td>V</td>
<td>7</td>
</tr>
<tr>
<td>Supply Current 1</td>
<td>$I_{\text{DD1}}$</td>
<td>$V_{\text{ENP}} = 1.5,\text{V}$, $V_{\text{ENN}} = V_{\text{FBP}} = 0,\text{V}$, $V_{\text{FBN}} = 0.1,\text{V}$ $V_{\text{IN}}$ is increasing from 0.3V.</td>
<td>50</td>
<td>170</td>
<td>450</td>
<td>μA</td>
<td>1</td>
</tr>
<tr>
<td>Supply Current 2</td>
<td>$I_{\text{DD2}}$</td>
<td>$V_{\text{IN}} = 5.5,\text{V}$, $V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V}$, $V_{\text{FBN}} = -0.1,\text{V}$</td>
<td>30</td>
<td>90</td>
<td>250</td>
<td>μA</td>
<td>1</td>
</tr>
<tr>
<td>Supply Current 3</td>
<td>$I_{\text{DD3}}$</td>
<td>$V_{\text{ENP}} = 0,\text{V}$, $V_{\text{FBP}} = 5.5,\text{V}$, $V_{\text{FBN}} = -0.1,\text{V}$</td>
<td>30</td>
<td>110</td>
<td>250</td>
<td>μA</td>
<td>1</td>
</tr>
<tr>
<td>Stand-by Current</td>
<td>$I_{\text{STB}}$</td>
<td>$V_{\text{IN}} = 5.5,\text{V}$, $V_{\text{ENP}} = 0,\text{V}$</td>
<td>-</td>
<td>0</td>
<td>1.0</td>
<td>μA</td>
<td>1</td>
</tr>
<tr>
<td>ENP “H” Voltage</td>
<td>$V_{\text{ENPH}}$</td>
<td>$V_{\text{IN}} = 5.5,\text{V}$, $V_{\text{ENP}} = 0,\text{V}$, $V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V}$</td>
<td>1.4</td>
<td>-</td>
<td>5.5</td>
<td>V</td>
<td>7</td>
</tr>
<tr>
<td>ENP “L” Voltage</td>
<td>$V_{\text{ENPL}}$</td>
<td>$V_{\text{IN}} = 5.5,\text{V}$, $V_{\text{ENP}} = 0,\text{V}$, $V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V}$</td>
<td>AGND</td>
<td>-</td>
<td>0.3</td>
<td>V</td>
<td>7</td>
</tr>
<tr>
<td>ENP “H” Current</td>
<td>$I_{\text{ENPH}}$</td>
<td>$V_{\text{ENP}} = 5.5,\text{V}$</td>
<td>-0.1</td>
<td>-</td>
<td>0.1</td>
<td>μA</td>
<td>7</td>
</tr>
<tr>
<td>ENP “L” Current</td>
<td>$I_{\text{ENPL}}$</td>
<td>$V_{\text{ENP}} = 0,\text{V}$</td>
<td>-0.1</td>
<td>-</td>
<td>0.1</td>
<td>μA</td>
<td>7</td>
</tr>
<tr>
<td>ENN “H” Voltage</td>
<td>$V_{\text{ENNH}}$</td>
<td>$V_{\text{ENP}} = 5.5,\text{V}$, $V_{\text{ENP}} = 0,\text{V}$, $V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V}$</td>
<td>1.4</td>
<td>-</td>
<td>5.5</td>
<td>V</td>
<td>7</td>
</tr>
<tr>
<td>ENN “L” Voltage</td>
<td>$V_{\text{ENNL}}$</td>
<td>$V_{\text{IN}} = 5.5,\text{V}$, $V_{\text{ENP}} = 0,\text{V}$, $V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V}$</td>
<td>AGND</td>
<td>-</td>
<td>0.3</td>
<td>V</td>
<td>7</td>
</tr>
<tr>
<td>ENN “H” Current</td>
<td>$I_{\text{ENNH}}$</td>
<td>$V_{\text{ENP}} = 5.5,\text{V}$</td>
<td>-0.1</td>
<td>-</td>
<td>0.1</td>
<td>μA</td>
<td>7</td>
</tr>
<tr>
<td>ENN “L” Current</td>
<td>$I_{\text{ENNL}}$</td>
<td>$V_{\text{ENP}} = 0,\text{V}$</td>
<td>-0.1</td>
<td>-</td>
<td>0.1</td>
<td>μA</td>
<td>7</td>
</tr>
<tr>
<td>MODE “H” Voltage</td>
<td>$V_{\text{MODEH}}$</td>
<td>$V_{\text{ENP}} = 5.5,\text{V}$, $V_{\text{ENN}} = 0,\text{V}$, $V_{\text{FBP}} = 5.5,\text{V}$ $V_{\text{MODE}}$ is increasing from 0.3V.</td>
<td>1.4</td>
<td>-</td>
<td>5.5</td>
<td>V</td>
<td>7</td>
</tr>
<tr>
<td>MODE “L” Voltage</td>
<td>$V_{\text{MODEL}}$</td>
<td>$V_{\text{ENP}} = 5.5,\text{V}$, $V_{\text{ENN}} = 0,\text{V}$, $V_{\text{FBP}} = 5.5,\text{V}$ $V_{\text{MODE}}$ is decreasing from 1.4V.</td>
<td>AGND</td>
<td>-</td>
<td>0.3</td>
<td>V</td>
<td>7</td>
</tr>
<tr>
<td>MODE “H” Current</td>
<td>$I_{\text{MODEH}}$</td>
<td>$V_{\text{ENP}} = 5.5,\text{V}$, $V_{\text{ENN}} = 0,\text{V}$, $V_{\text{FBP}} = 5.5,\text{V}$ $V_{\text{MODE}}$ is increasing from 0.3V.</td>
<td>-0.1</td>
<td>-</td>
<td>0.1</td>
<td>μA</td>
<td>7</td>
</tr>
<tr>
<td>MODE “L” Current</td>
<td>$I_{\text{MODEL}}$</td>
<td>$V_{\text{ENP}} = 5.5,\text{V}$, $V_{\text{ENN}} = 0,\text{V}$, $V_{\text{FBP}} = 5.5,\text{V}$ $V_{\text{MODE}}$ is decreasing from 1.4V.</td>
<td>-0.1</td>
<td>-</td>
<td>0.1</td>
<td>μA</td>
<td>7</td>
</tr>
<tr>
<td>FBP “H” Current</td>
<td>$I_{\text{FBPH}}$</td>
<td>$V_{\text{ENP}} = 5.5,\text{V}$, $V_{\text{ENN}} = 0,\text{V}$, $V_{\text{FBP}} = 0,\text{V}$</td>
<td>-0.1</td>
<td>-</td>
<td>0.1</td>
<td>μA</td>
<td>7</td>
</tr>
<tr>
<td>FBP “L” Current</td>
<td>$I_{\text{FBPL}}$</td>
<td>$V_{\text{ENP}} = 5.5,\text{V}$, $V_{\text{CONN}} = 0,\text{V}$, $V_{\text{FBP}} = 0,\text{V}$</td>
<td>-0.1</td>
<td>-</td>
<td>0.1</td>
<td>μA</td>
<td>7</td>
</tr>
<tr>
<td>FBN “H” Current</td>
<td>$I_{\text{FBNH}}$</td>
<td>$V_{\text{ENP}} = 5.5,\text{V}$, $V_{\text{ENN}} = 0,\text{V}$, $V_{\text{FBN}} = 5.5,\text{V}$</td>
<td>-0.1</td>
<td>-</td>
<td>0.1</td>
<td>μA</td>
<td>7</td>
</tr>
<tr>
<td>FBN “L” Current</td>
<td>$I_{\text{FBNL}}$</td>
<td>$V_{\text{ENP}} = 5.5,\text{V}$, $V_{\text{ENN}} = 0,\text{V}$, $V_{\text{FBN}} = 0,\text{V}$</td>
<td>-0.1</td>
<td>-</td>
<td>0.1</td>
<td>μA</td>
<td>7</td>
</tr>
<tr>
<td>SWP “H” Current</td>
<td>$I_{\text{SWPH}}$</td>
<td>$V_{\text{ENP}} = 5.5,\text{V}$, $V_{\text{ENN}} = 0,\text{V}$, $V_{\text{SWP}} = 5.5,\text{V}$</td>
<td>-0.1</td>
<td>-</td>
<td>0.1</td>
<td>μA</td>
<td>7</td>
</tr>
<tr>
<td>SWP “L” Current</td>
<td>$I_{\text{SWPL}}$</td>
<td>$V_{\text{ENP}} = 5.5,\text{V}$, $V_{\text{ENN}} = 0,\text{V}$, $V_{\text{SWP}} = 0,\text{V}$</td>
<td>-0.1</td>
<td>-</td>
<td>0.1</td>
<td>μA</td>
<td>7</td>
</tr>
<tr>
<td>Integral Latch Time</td>
<td>$t_{\text{LAT}}$</td>
<td>$V_{\text{ENP}} = 5.5,\text{V}$, $V_{\text{ENN}} = 0,\text{V}$, $V_{\text{FBP}} = 0,\text{V}$ $V_{\text{FBN}} = 0.1,\text{V}$ Time to stop operation from the start of maximum current limit status.</td>
<td>1.0</td>
<td>2.0</td>
<td>3.0</td>
<td>ms</td>
<td>5</td>
</tr>
<tr>
<td>Thermal Shutdown Temperature</td>
<td>$T_{\text{TSO}}$</td>
<td>-</td>
<td>150</td>
<td>-</td>
<td>°C</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>Thermal Shutdown Release Temperature</td>
<td>$T_{\text{TSO}}$</td>
<td>-</td>
<td>130</td>
<td>-</td>
<td>°C</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>Thermal Shutdown Hysteresis Range</td>
<td>$T_{\text{HYS}}$</td>
<td>$T_{\text{HYS}} - T_{\text{TSO}} - T_{\text{TSO}}$</td>
<td>-</td>
<td>20</td>
<td>-</td>
<td>°C</td>
<td>-</td>
</tr>
</tbody>
</table>

(1) If the applied voltage and its pin name are not stated, those pins are left open for measurement.
## ELECTRICAL CHARACTERISTICS (Continued)

### XC9519 Series, Step-up DC/DC Converter

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SYMBOL</th>
<th>CONDITIONS ((^{(*)}))</th>
<th>MIN.</th>
<th>TYP.</th>
<th>MAX.</th>
<th>UNITS</th>
<th>CIRCUIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Output Voltage Range</td>
<td>(V_{\text{OUTPSET}})</td>
<td>(V_{\text{IN}} = V_{\text{ENP}} = 3.6,\text{V}, V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V})</td>
<td>4.0 (^{(2)})</td>
<td>-</td>
<td>18.0</td>
<td>(\text{V})</td>
<td>-</td>
</tr>
<tr>
<td>FBP Voltage</td>
<td>(V_{\text{FBP}})</td>
<td>(V_{\text{IN}} = V_{\text{ENP}} = 3.6,\text{V}, V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V})</td>
<td>0.985</td>
<td>1.000</td>
<td>1.015</td>
<td>(\text{V})</td>
<td>(3)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>The voltage which (L_{\text{XP}}) starts oscillation while (V_{\text{FBP}}) is decreasing.</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Oscillation Frequency</td>
<td>(f_{\text{OSC}})</td>
<td>(V_{\text{IN}} = V_{\text{ENP}} = 3.6,\text{V}, V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V})</td>
<td>1020</td>
<td>1200</td>
<td>1380</td>
<td>kHz</td>
<td>(3)</td>
</tr>
<tr>
<td>PFM Switching Current</td>
<td>(I_{\text{PFM}})</td>
<td>(V_{\text{IN}} = V_{\text{ENP}} = 3.6,\text{V}, V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V})</td>
<td>180</td>
<td>350</td>
<td>550</td>
<td>mA</td>
<td>(8)</td>
</tr>
<tr>
<td>Maximum Duty Cycle</td>
<td>(D_{\text{MAXP}})</td>
<td>(V_{\text{IN}} = V_{\text{ENP}} = 3.6,\text{V}, V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V})</td>
<td>84</td>
<td>90</td>
<td>97</td>
<td>%</td>
<td>(3)</td>
</tr>
<tr>
<td>LXP SW &quot;H&quot; ON Resistance</td>
<td>(R_{\text{LXP}})</td>
<td>(V_{\text{IN}} = V_{\text{ENP}} = 3.6,\text{V}, V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V}, I_{\text{LXP}} = 100,\text{mA})</td>
<td>-</td>
<td>0.12</td>
<td>0.28</td>
<td>(\Omega)</td>
<td>(4)</td>
</tr>
<tr>
<td>LXP SW &quot;H&quot; Leak Current</td>
<td>(I_{\text{LEAKH}})</td>
<td>(V_{\text{IN}} = 5.5,\text{V}, V_{\text{ENP}} = 0,\text{V}, V_{\text{LXP}} = 5.5,\text{V})</td>
<td>-</td>
<td>0.01</td>
<td>1.0</td>
<td>(\mu,\text{A})</td>
<td>(5)</td>
</tr>
<tr>
<td>Maximum Current Limit (^{(3)})</td>
<td>(I_{\text{LMP}})</td>
<td>(V_{\text{IN}} = V_{\text{ENP}} = 5.5,\text{V}, V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V})</td>
<td>2000</td>
<td>-</td>
<td>4000</td>
<td>mA</td>
<td>(5)</td>
</tr>
<tr>
<td>FBP Voltage Temperature Characteristics</td>
<td>(\Delta V_{\text{FBP}} / (V_{\text{FBP}} \cdot \Delta \text{Topr}))</td>
<td>(-40^\circ\text{C} \leq \text{Topr} \leq 85^\circ\text{C})</td>
<td>-</td>
<td>±100</td>
<td>-</td>
<td>ppm /(^{\circ}\text{C})</td>
<td>-</td>
</tr>
<tr>
<td>Soft-Start Time</td>
<td>(t_{\text{SS}})</td>
<td>(V_{\text{IN}} = 3.6,\text{V}, V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V})</td>
<td>0.8</td>
<td>2.5</td>
<td>5.2</td>
<td>ms</td>
<td>(3)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(V_{\text{OUTP}} = V_{\text{SWP}} = 3.6,\text{V}, V_{\text{FBP}} = 0.95,\text{V}) (\text{Time to start (L_{\text{XP}}) oscillation from the rise of } V_{\text{ENP}})</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>(0,\text{V} \rightarrow 3.6,\text{V})</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Short Protection Threshold Voltage</td>
<td>(V_{\text{SHORTP}})</td>
<td>(V_{\text{IN}} = V_{\text{ENP}} = 5.5,\text{V}, V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V})</td>
<td>0.3</td>
<td>0.5</td>
<td>0.7</td>
<td>(\text{V})</td>
<td>(5)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(V_{\text{OUTP}} = V_{\text{SWP}} = 5.5,\text{V}) (\text{The voltage which the integral latch time becomes } 200,\mu\text{s or less while } V_{\text{FBP}} \text{ is decreasing.})</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Over Voltage Protection Limit</td>
<td>(V_{\text{OVPP}})</td>
<td>(V_{\text{IN}} = V_{\text{ENP}} = 3.6,\text{V}, V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V})</td>
<td>(V_{\text{FBP}} + 0.03)</td>
<td>(V_{\text{FBP}} + 0.07)</td>
<td>(V_{\text{FBP}} + 0.10)</td>
<td>(\text{V})</td>
<td>(3)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(V_{\text{OUTP}} = V_{\text{SWP}} = 3.6,\text{V}, V_{\text{GAINP}} = 3.6,\text{V}) (\text{The voltage which } L_{\text{XP}} \text{ stops oscillation while } V_{\text{FBP}} \text{ is increasing.})</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>BSW Pin Current</td>
<td>(I_{\text{BSW}})</td>
<td>(V_{\text{IN}} = 3.6,\text{V}, V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V})</td>
<td>0.2</td>
<td>1.2</td>
<td>3.0</td>
<td>mA</td>
<td>(7)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(V_{\text{OUTP}} = V_{\text{SWP}} = 3.6,\text{V}, V_{\text{BSW}} = 3.6,\text{V})</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>(C_{\text{D}}) Discharge Resistance</td>
<td>(R_{\text{GCHGP}})</td>
<td>(V_{\text{IN}} = 6.0,\text{V}, V_{\text{ENP}} = V_{\text{ENN}} = V_{\text{MODE}} = 0,\text{V})</td>
<td>50</td>
<td>200</td>
<td>500</td>
<td>(\Omega)</td>
<td>(2)</td>
</tr>
<tr>
<td></td>
<td></td>
<td>(V_{\text{OUTP}} = 4.0,\text{V})</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

**NOTE:**

(\(^{(1)}\) If the applied voltage and its pin name are not stated, those pins are left open for measurement.
(\(^{(2)}\) Input voltage or positive output voltage range should be \(V_{\text{OUTPSET}} \geq V_{\text{IN}} + 0.2\,\text{V}\).
(\(^{(3)}\) Maximum current limit denotes the level of detection at peak of coil current.)
### ELECTRICAL CHARACTERISTICS (Continued)

#### XC9519 Series, Inverting DC/DC Converter

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SYMBOL</th>
<th>CONDITIONS (1)</th>
<th>MIN.</th>
<th>TYP.</th>
<th>MAX.</th>
<th>UNITS</th>
<th>CIRCUIT</th>
</tr>
</thead>
</table>
| Output Voltage Range          | V\(_{\text{OUTSET}}\) | V\(_{\text{IN}}\) = V\(_{\text{ENP}}\) = V\(_{\text{MODE}}\) = 0V  
\(\text{The voltage which L\(_{\text{LN}}\) starts oscillation while} 
V\(_{\text{FBIN}}\) \text{ is increasing.}\) | -15.0\(^{(2)}\) | - | -4.0 V | - | - |
| FBN Voltage                   | V\(_{FBIN}\)  | V\(_{\text{IN}}\) = V\(_{\text{ENP}}\) = 3.6V, V\(_{\text{ENP}}\) = V\(_{\text{MODE}}\) = 0V  
\(\text{The voltage which L\(_{\text{LN}}\) starts oscillation while} 
V\(_{\text{FBIN}}\) \text{ is increasing.}\) | -26 | 0 | 26 mV | 3 |
| Reference Voltage             | V\(_{\text{REF}}\) | V\(_{\text{IN}}\) = V\(_{\text{ENP}}\) = 3.6V, V\(_{\text{ENP}}\) = V\(_{\text{MODE}}\) = 0V, V\(_{\text{FBIN}}\) = 0.1V | 0.970 | 1.000 | 1.030 V | 1 |
| Output Voltage Accuracy       | V\(_{\text{OUTNA}}\) | V\(_{\text{OUTN}}\) = V\(_{\text{REF}}\) - V\(_{FBIN}\) | 0.985 | 1.000 | 1.015 V | - |
| Oscillation Frequency         | f\(_{\text{OSC}}\) | V\(_{\text{IN}}\) = V\(_{\text{ENP}}\) = 3.6V, V\(_{\text{ENP}}\) = V\(_{\text{MODE}}\) = 0V, V\(_{\text{FBIN}}\) = 0.1V | 1020 | 1200 | 1380 kHz | 3 |
| PFM Switching Current         | I\(_{PFM}\) | V\(_{IN}\) = V\(_{ENP}\) = V\(_{MODE}\) = 3.6V, V\(_{ENP}\) = 0V | 220 | 350 | 550 mA | 8 |
| Maximum Duty Cycle            | D\(_{\text{MAX}}\) | V\(_{IN}\) = V\(_{ENP}\) = 3.6V, V\(_{ENP}\) = V\(_{MODE}\) = 0V, V\(_{FBIN}\) = 0.1V | 84 | 90 | 97 % | 3 |
| LXN SW "L" ON Resistance      | R\(_{\text{LXN}}\) | V\(_{IN}\) = V\(_{ENP}\) = 3.6V, V\(_{ENP}\) = V\(_{MODE}\) = 0V, I\(_{LXN}\) = 100mA | - | 0.22 | 0.48 Ω | 4 |
| LXN SW "L" Leak Current       | I\(_{\text{LEAKL}}\) | V\(_{IN}\) = V\(_{ENP}\) = 3.6V, V\(_{ENP}\) = V\(_{MODE}\) = 0V, V\(_{FBIN}\) = 0.1V | - | 0.01 | 1.0 μA | 6 |
| Maximum Current Limit (\*3)   | I\(_{\text{LIMN}}\) | V\(_{IN}\) = V\(_{ENP}\) = 5.5V, V\(_{ENP}\) = V\(_{MODE}\) = 0V  
V\(_{FBIN}\) = 0.1V | 2000 | - | 4000 mA | 5 |
| Reference Voltage Temperature Characteristics | \(\Delta V_{\text{REF}} / (V_{\text{REF}} \cdot \Delta T_{\text{TOP}})\) | - | ±100 | - | ppm / °C | - |
| Soft-Start Time               | t\(_{SSN}\) | V\(_{IN}\) = 3.6V, V\(_{ENP}\) = V\(_{MODE}\) = 0V, V\(_{FBIN}\) = 0.05V  
Time to start L\(_{\text{LN}}\) oscillation from the rise of V\(_{ENP}\).  
(0V—3.6V) | 0.8 | 2.2 | 4.0 ms | 3 |
| Short Protection Threshold Voltage | V\(_{\text{SHORTN}}\) | V\(_{IN}\) = V\(_{ENP}\) = 5.5V, V\(_{ENP}\) = V\(_{MODE}\) = 0V  
The voltage which the integral latch time  
becomes 200 μs or less while V\(_{FBIN}\) is increasing. | 0.3 | 0.5 | 0.7 V | 5 |
| Over Voltage Protection Limit  | V\(_{\text{OPN}}\) | V\(_{IN}\) = V\(_{ENP}\) = 3.6V, V\(_{ENP}\) = V\(_{MODE}\) = 0V, V\(_{GAINN}\) = 3.6V  
The voltage which L\(_{\text{LN}}\) stops oscillation while  
V\(_{FBIN}\) is decreasing. | V\(_{FBIN}\) -0.10 | V\(_{FBIN}\) -0.07 | V\(_{FBIN}\) -0.03 V | 3 |
| CL Discharge Resistance       | R\(_{\text{DCHG}}\) | V\(_{IN}\) = 6.0V, V\(_{ENP}\) = V\(_{ENP}\) = V\(_{MODE}\) = 0V  
V\(_{\text{OUTN}}\) = -4.0V | 50 | 200 | 500 Ω | 2 |

**NOTE:**

(\*1) If the applied voltage and its pin name are not stated, those pins are left open for measurement.

(\*2) Input voltage or positive output voltage range should be V\(_{IN}\) - V\(_{\text{OUTSET}}\) + V\(_{FBIN}\) \(\leq\) 21.0V (V\(_{FBIN}\): Forward voltage of external schottky barrier diode).

(\*3) Maximum current limit denotes the level of detection at peak of coil current.
**OPERATIONAL EXPLANATION**

The XC9519 series consists of a reference voltage source, ramp wave circuit, error amplifier, PWM comparator, phase compensation circuit, driver transistor, current limiter circuit, short protection circuit, UVLO circuit, thermal shutdown circuit, over voltage protection, load disconnect control and others. (See the block diagram below.)

By using the error amplifier, the FBP (FBN) pin voltage is compared with the internal reference voltage. The error amplifier output is sent to the PWM comparator in order to determine the duty cycle of PWM switching. The signal from the error amplifier is compared with the ramp wave from the ramp wave circuit, and the resulting output is delivered to the buffer driver circuit to provide on-time of the duty cycle at the LXP (LXN) pin. This process is continuously performed to ensure stable output voltage.

The current feedback circuit monitors the driver transistor current for each switching operation, and modulates the error amplifier output signal to provide multiple feedback signals. This enables a stable feedback loop even when using a low ESR capacitor such as ceramic, which results in ensuring stable output voltage.

* Diodes inside the circuit are an ESD protection diode and a parasitic diode.
OPERATIONAL EXPLANATION (Continued)

<Reference Voltage Source>
The reference voltage source provides the reference voltage to ensure stable output voltage of the DC/DC converter.

<Step-up DC/DC Converter Error Amplifier>
The step-up DC/DC converter error amplifier is an amplifier for output voltage monitoring. The FBP pin voltage is compared to the reference voltage. When a voltage lower than the reference voltage is feedback to the FBP pin voltage, the output voltage of the error amplifier goes high. External compensation of the error amplifier frequency characteristic is also possible.

<Inverting DC/DC Converter Error Amplifier>
The inverting DC/DC converter error amplifier is an amplifier for output voltage monitoring. The FBN pin voltage is compared to GND. When a voltage higher than GND is feedback to the FBN pin voltage, the output voltage of the error amplifier goes high. External compensation of the error amplifier frequency characteristic is also possible.

<Ramp Wave Circuit>
The ramp wave circuit determines switching frequency. The frequency is fixed 1.2MHz internally. Clock pulses generated in this circuit are used to produce ramp waveforms needed for PWM operation, and to synchronize all the internal circuits.

<UVLO Circuit>
When the AVIN pin voltage becomes 2.2V or lower, the driver transistor is forced OFF to prevent false pulse output caused by unstable operation of the internal circuitry. When the AVIN pin voltage becomes 2.4V or higher, switching operation takes place. By releasing the UVLO function, the IC performs the soft start function to initiate output startup operation. The soft start function operates even when the AVIN pin voltage falls momentarily below the UVLO detect voltage. The UVLO circuit does not cause a complete shutdown of the IC, but causes pulse output to be suspended; therefore, the internal circuitry remains in operation.

<Thermal Shutdown>
For protection against heat damage of the ICs, thermal shutdown function monitors chip temperature. The thermal shutdown circuit starts operating and the driver transistor will be turned off when the chip’s temperature reaches 150°C. When the temperature drops to 130°C or less after shutting of the current flow, the IC performs the soft start function to initiate output startup operation.

<PFM Switch Current>
In PFM control operation, until coil current reaches to a specified level (IPFMP, IPFMN), the IC keeps the driver transistor on. In this case, time (tON) that the driver transistor is kept on can be given by the following formula.

\[ t_{ON} = \frac{L \times V_{IN}}{I_{MP}} \]

< PFM Duty Limit >
In PFM control operation, the maximum duty cycle (DTY LIMIT_PFM) is set to 50% (TYP.). Therefore, under the condition that the duty increases (e.g. the condition that the step-up ratio is large), it’s possible for the driver transistor to be turned off even when the coil current doesn’t reach to IPFMP (IPFMN).
< Operational Explanation (Continued) >

< CL Auto-Discharge Function >
This function enables high-speed discharge of the charge on the output capacitor (CL) when an L level signal is input to the ENP (ENN) pin by means of the internal switch between the VOUTP pin and AGND pin (between the VOUTN pin and AVIN pin).
This function makes it possible to prevent malfunctioning of applications caused by charge remaining on CL.
The discharge time is determined by the CL discharge resistance (RDCHG) and CL. If \( \tau = CL \times RDCHG \) is the time constant of CL and RDCHG, the equation for the output voltage discharge time can be obtained from the following CR discharge equation.

\[
t = \tau \ln \left( \frac{V_{OUTSET}}{V} \right)
\]

\( V \): Output voltage during discharge
\( V_{OUTSET} \): Output voltage
\( t \): Discharge time
\( \tau \): \( CL \times RDCHG \)

[Example]
When the set voltage (\( V_{OUTSET} \))=5.0V, CL=18.8 \( \mu \)F, and the CL discharge resistance (RDCHG)=200 \( \Omega \) (TYP.) of the DC/DC Converter, the discharge time \( t \) from the start of CL high-speed discharge until the output voltage falls to 1.0V can be calculated as follows:

\[
t = \tau \ln \left( \frac{V_{OUTSET}}{V} \right) = CL \times RDCHG \ln \left( \frac{V_{OUTSET}}{V} \right) = 18.8 \mu F \times 200 \Omega \times \ln \left( \frac{5.0V}{1.0V} \right) = 6.05 \times 10^{-3} s = 6.05 ms \quad (*1)
\]

(*1) Calculated with \( I_{OUT} = 0mA \)

< Internal OSC Timing Chart >
The step-up DC/DC Converter and the Inverting DC/DC Converter are switching synchronously based on one internal clock. The phase of the step-up driver on timing for the DC/DC Converter is shifted to completely opposite position (180 degrees different) upon the phase of driver on timing for the Inverting DC/DC Converter.

< Overvoltage Protection >
Overvoltage protection monitors the output voltage \( V_{OUTP} (V_{OUTN}) \) using the FBP (FBN) pin voltage, and prevents the output voltage \( V_{OUTP} (V_{OUTN}) \) from rising too far above the set voltage. In particular, fluctuations in the load cause the output voltage to rise, and when the FBP (FBN) pin voltage reaches the overvoltage protection detection voltage, the driver transistor of the step-up DC/DC converter (inverting DC/DC converter) is turned off to hold down the rise of output voltage. When the output voltage falls after overvoltage protection detection, normal DC/DC converter operation resumes.
The output voltage \( V_{OUT_OVP} \) that is detected by overvoltage protection is obtained from the following equation:

\[
V_{OUT_OVP} = V_{OUTSET} \times V_{OVPP} / V_{FBP}
\]

\( V_{OUTSET} \): Output voltage, \( V_{OVPP} \): Detect Overvoltage Protection Voltage

[Example]
In a step-up DC/DC converter with the indicated conditions, the output voltage \( V_{OUT_OVP} \) that is detected by overvoltage protection can be calculated as shown below.

**Condition**: Output Voltage (\( V_{OUTSET} \))=5.0V, \( V_{OVPP} = V_{FBP} = 0.07V \) (TYP.), \( V_{FBP} = 1.0V \) (TYP.)

\[
V_{OUT_OVP} = V_{OUTSET} \times V_{OVPP} = 5.0V \times (1.0 + 0.07(TYP.)) = 5.0V \times 1.07 = 5.35V
\]

< Load disconnect Control Circuit >
The Load disconnect control circuit makes it possible to break continuity between \( V_{IN} \) and \( V_{OUT} \) by turning off the external P-ch MOS FET when the step-up DC/DC converter is in the standby state.
OPERATIONAL EXPLANATION (Continued)

<Current Limit>
The current limiter circuit of the XC9519 series monitors the current flowing through the driver transistor, and features a combination of the current limit mode and the operation suspension mode.

1. When the driver current is greater than a specific level, the current limit function operates to turn off the pulses from the LXP (LXN) pin at any given timing.
2. When the driver transistor is turned off, the limiter circuit is then released from the current limit detection state.
3. At the next pulse, the driver transistor is turned on. However, the transistor is immediately turned off in the case of an over current state.
4. When the over current state is eliminated, the IC resumes its normal operation.

The IC waits for the over current state to end by repeating the steps 1 through 3. If an over current state continues for the integral latch time and the above three steps are repeatedly performed, the IC performs the function of integral latching the OFF state of the driver transistor, and goes into operation suspension mode.

Once the IC is in suspension mode, operations can be resumed by either turning the IC off via the ENP (ENN) pin, or by restoring power. Care must be taken when laying out the PC Board, in order to prevent misoperation of the current limit mode. Depending on the state of the PC Board, latch time may become longer and latch operation may not work. In order to avoid the effect of noise, the board should be laid out so that input capacitors are placed as close to the IC as possible.

(a) Step-up DC/DC Converter

(b) Inverting DC/DC Converter

<Short-Circuit Protection>
The short-circuit protection circuit monitors the output voltage from the VOUTP (VOUTN). In case where output is accidentally shorted to the GND and when the FBP voltage decreases less than short protection threshold voltage or FBN pin voltage becomes larger than short protection threshold voltage and a current more than the I_{LM} flows to the driver transistor, the short-circuit protection quickly operates to turn off and to latch the driver transistor.

Once the IC is in suspension mode, operations can be resumed by either turning the IC off via the ENP (ENN) pin, or by restoring power.
**EXTERNAL COMPONENTS**

*<Step-up DC/DC Converter Output Voltage Setting>*

The output voltage $V_{OUTP}$ of a step-up DC/DC converter can be set by connecting external dividing resistors $R_{FBP1}$ and $R_{FBP2}$. The output voltage $V_{OUTP}$ is determined by the values of $R_{FBP1}$ and $R_{FBP2}$ as given in the equation below.

Adjust $R_{FBP1}$ and $R_{FBP2}$ so that $(R_{FBP1} + R_{FBP2}) < 500k\Omega$.

\[
V_{OUTP} = V_{FBP} \times \frac{(R_{FBP1} + R_{FBP2})}{R_{FBP2}}
\]

Set the output voltage so that $V_{OUTP} \geq V_{IN} + 0.2V$ is satisfied.

Adjust the value of the phase compensation speed-up capacitor $C_{FBP}$ so that $f_{zp} = \frac{1}{(2 \times \pi \times R_{FBP1})}$ is about 40kHz, and insert several kΩ in series as $R_{SP}$. If a high output voltage is set, inserting a phase compensation speed-up capacitor may cause unstable operation.

Examples of setting $C_{FBP}$ and $R_{SP}$ are shown in the next section, “Step-up DC/DC Converter Error Amplifier External Compensation”.

**Typical Examples**

<table>
<thead>
<tr>
<th>$V_{OUTP}$</th>
<th>$R_{FBP1}$</th>
<th>$R_{FBP2}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>4.0V</td>
<td>300kΩ</td>
<td>100kΩ</td>
</tr>
<tr>
<td>5.0V</td>
<td>300kΩ</td>
<td>75kΩ</td>
</tr>
<tr>
<td>9.0V</td>
<td>240kΩ</td>
<td>30kΩ</td>
</tr>
<tr>
<td>12.0V</td>
<td>330kΩ</td>
<td>30kΩ</td>
</tr>
<tr>
<td>15.0V</td>
<td>336kΩ</td>
<td>24kΩ</td>
</tr>
<tr>
<td>18.0V</td>
<td>408kΩ</td>
<td>24kΩ</td>
</tr>
</tbody>
</table>

*<Inverting DC/DC Converter Output Voltage Setting>*

The output voltage $V_{OUTN}$ of an inverting DC/DC converter can be set by connecting external dividing resistors $R_{FBN1}$ and $R_{FBN2}$. The output voltage $V_{OUTN}$ is determined by the values of $R_{FBN1}$ and $R_{FBN2}$ as given in the equation below.

Adjust $R_{FBN1}$ and $R_{FBN2}$ so that $(R_{FBN1} + R_{FBN2}) < 500k\Omega$.

\[
V_{OUTN} = -\frac{(V_{REF} - V_{FBN}) \times R_{FBN1}}{R_{FBN2}}
\]

Set the output voltage so that $V_{IN} - V_{OUTN} + V_{FN} \leq 21.0V$

($V_{FN}$: Forward voltage of external diode SBDn) is satisfied.

**Typical Examples**

<table>
<thead>
<tr>
<th>$V_{OUTN}$</th>
<th>$R_{FBN1}$</th>
<th>$R_{FBN2}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>-4.0V</td>
<td>300kΩ</td>
<td>75kΩ</td>
</tr>
<tr>
<td>-5.0V</td>
<td>300kΩ</td>
<td>60kΩ</td>
</tr>
<tr>
<td>-9.0V</td>
<td>270kΩ</td>
<td>30kΩ</td>
</tr>
<tr>
<td>-12.0V</td>
<td>360kΩ</td>
<td>30kΩ</td>
</tr>
<tr>
<td>-15.0V</td>
<td>360kΩ</td>
<td>24kΩ</td>
</tr>
</tbody>
</table>
COMPONENT SELECTION METHOD (Continued)

<Step-up DC/DC Converter Error Amplifier External Compensation>

External compensation of the frequency characteristic of a step-up DC/DC converter error amplifier is possible with $R_{ZP}$ and $C_{ZP}$. The values of $R_{ZP}$ and $C_{ZP}$ can be adjusted to obtain the optimum load-transient response (step response). For adjustment using the input voltage and output voltage, use the setting values below.

### Li-ion (2.7~4.4V)

<table>
<thead>
<tr>
<th>$V_{IN}$</th>
<th>Output Voltage Range</th>
<th>$L_P$</th>
<th>$C_{LP}$</th>
<th>$R_{ZP}$</th>
<th>$C_{ZP}$</th>
<th>$C_{FBP}$</th>
<th>$R_{SP}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>4.6V ≤ $V_{OUTP}$ ≤ 5.0V</td>
<td>3.3μH</td>
<td>2×4.7 μF</td>
<td>5.1kΩ</td>
<td>4.7nF</td>
<td>47pF$^{(1)}$</td>
<td>4.7kΩ</td>
<td></td>
</tr>
<tr>
<td>5.0V &lt; $V_{OUTP}$ ≤ 9.0V</td>
<td>3.3μH</td>
<td>2×4.7 μF</td>
<td>5.1kΩ</td>
<td>4.7nF</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>9.0V &lt; $V_{OUTP}$ ≤ 12.0V</td>
<td>3.3μH</td>
<td>2×4.7 μF</td>
<td>10kΩ</td>
<td>4.7nF</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>12.0V &lt; $V_{OUTP}$ ≤ 15.0V</td>
<td>3.3μH</td>
<td>2×4.7 μF</td>
<td>16kΩ</td>
<td>2.2nF</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>15.0V &lt; $V_{OUTP}$ ≤ 18.0V</td>
<td>3.3μH</td>
<td>4×4.7 μF</td>
<td>24kΩ</td>
<td>4.7nF</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
</tbody>
</table>

### 3.3V±10%

<table>
<thead>
<tr>
<th>$V_{IN}$</th>
<th>Output Voltage Range</th>
<th>$L_P$</th>
<th>$C_{LP}$</th>
<th>$R_{ZP}$</th>
<th>$C_{ZP}$</th>
<th>$C_{FBP}$</th>
<th>$R_{SP}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>4.0V ≤ $V_{OUTP}$ ≤ 5.0V</td>
<td>3.3μH</td>
<td>2×4.7 μF</td>
<td>8.2kΩ</td>
<td>4.7nF</td>
<td>47pF$^{(2)}$</td>
<td>4.7kΩ</td>
<td></td>
</tr>
<tr>
<td>5.0V &lt; $V_{OUTP}$ ≤ 9.0V</td>
<td>3.3μH</td>
<td>2×4.7 μF</td>
<td>16kΩ</td>
<td>2.2nF</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>9.0V &lt; $V_{OUTP}$ ≤ 12.0V</td>
<td>3.3μH</td>
<td>2×4.7 μF</td>
<td>22kΩ</td>
<td>2.2nF</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>12.0V &lt; $V_{OUTP}$ ≤ 15.0V</td>
<td>3.3μH</td>
<td>4×4.7 μF</td>
<td>30kΩ</td>
<td>2.2nF</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>15.0V &lt; $V_{OUTP}$ ≤ 18.0V</td>
<td>3.3μH</td>
<td>4×4.7 μF</td>
<td>36kΩ</td>
<td>2.2nF</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
</tbody>
</table>

### 5V±10%

<table>
<thead>
<tr>
<th>$V_{IN}$</th>
<th>$V_{OUTP}$</th>
<th>$L_P$</th>
<th>$C_{LP}$</th>
<th>$R_{ZP}$</th>
<th>$C_{ZP}$</th>
<th>$C_{FBP}$</th>
<th>$R_{SP}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>5.7V ≤ $V_{OUTP}$ ≤ 7.0V</td>
<td>3.3μH</td>
<td>2×4.7 μF</td>
<td>4.7kΩ</td>
<td>4.7nF</td>
<td>68pF$^{(3)}$</td>
<td>4.7kΩ</td>
<td></td>
</tr>
<tr>
<td>7.0V &lt; $V_{OUTP}$ ≤ 9.0V</td>
<td>3.3μH</td>
<td>2×4.7 μF</td>
<td>5.1kΩ</td>
<td>4.7nF</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>9.0V &lt; $V_{OUTP}$ ≤ 12.0V</td>
<td>3.3μH</td>
<td>2×4.7 μF</td>
<td>8.2kΩ</td>
<td>4.7nF</td>
<td>68pF$^{(3)}$</td>
<td>4.7kΩ</td>
<td></td>
</tr>
<tr>
<td>12.0V &lt; $V_{OUTP}$ ≤ 15.0V</td>
<td>3.3μH</td>
<td>2×4.7 μF</td>
<td>13kΩ</td>
<td>2.2nF</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
<tr>
<td>15.0V &lt; $V_{OUTP}$ ≤ 18.0V</td>
<td>3.3μH</td>
<td>4×4.7 μF</td>
<td>18kΩ</td>
<td>2.2nF</td>
<td>-</td>
<td>-</td>
<td></td>
</tr>
</tbody>
</table>

(*1) Setting value with RFBP1 = 300kΩ
(*2) Setting value with RFBP1 = 360kΩ
(*3) Setting value with RFBP1 = 240kΩ
COMPONENT SELECTION METHOD (Continued)

<Inverting DC/DC Converter Error Amplifier External Compensation>

External compensation of the frequency characteristic of an inverting DC/DC converter error amplifier is possible with $R_{ZN}$ and $C_{ZN}$. The values of $R_{ZN}$ and $C_{ZN}$ can be adjusted to obtain the optimum load-transient response (step response). For adjustment using the input voltage and output voltage, use the setting values below.

<table>
<thead>
<tr>
<th>$V_{IN}$</th>
<th>Output Voltage Range</th>
<th>$L_{N}$</th>
<th>$C_{LN}$</th>
<th>$R_{ZN}$</th>
<th>$C_{ZN}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>Li-ion (2.7~4.4V)</td>
<td>$-4.0V \geq V_{OUTN} \geq -5.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$51k \Omega$</td>
<td>$1.0nF$</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$110k \Omega$</td>
</tr>
<tr>
<td></td>
<td>$-5.0V &gt; V_{OUTN} \geq -9.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$68k \Omega$</td>
<td>$0.47nF$</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$130k \Omega$</td>
</tr>
<tr>
<td></td>
<td>$-9.0V &gt; V_{OUTN} \geq -12.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$120k \Omega$</td>
<td>$0.47nF$</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$200k \Omega$</td>
</tr>
<tr>
<td></td>
<td>$-12.0V &gt; V_{OUTN} \geq -15.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$110k \Omega$</td>
<td>$1.0nF$</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$200k \Omega$</td>
</tr>
</tbody>
</table>

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<tr>
<th>$V_{IN}$</th>
<th>Output Voltage Range</th>
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<th>$C_{LN}$</th>
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<tbody>
<tr>
<td>Li-ion (2.7~4.4V)</td>
<td>$-4.0V \geq V_{OUTN} \geq -5.0V$</td>
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<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$110k \Omega$</td>
</tr>
<tr>
<td></td>
<td>$-5.0V &gt; V_{OUTN} \geq -9.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$68k \Omega$</td>
<td>$0.47nF$</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$130k \Omega$</td>
</tr>
<tr>
<td></td>
<td>$-9.0V &gt; V_{OUTN} \geq -12.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$120k \Omega$</td>
<td>$0.47nF$</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$200k \Omega$</td>
</tr>
<tr>
<td></td>
<td>$-12.0V &gt; V_{OUTN} \geq -15.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$110k \Omega$</td>
<td>$1.0nF$</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$200k \Omega$</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>$V_{IN}$</th>
<th>Output Voltage Range</th>
<th>$L_{N}$</th>
<th>$C_{LN}$</th>
<th>$R_{ZN}$</th>
<th>$C_{ZN}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>3.3V±10%</td>
<td>$-4.0V \geq V_{OUTN} \geq -5.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$51k \Omega$</td>
<td>$1.0nF$</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$110k \Omega$</td>
</tr>
<tr>
<td></td>
<td>$-5.0V &gt; V_{OUTN} \geq -9.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$68k \Omega$</td>
<td>$0.47nF$</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$130k \Omega$</td>
</tr>
<tr>
<td></td>
<td>$-9.0V &gt; V_{OUTN} \geq -12.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$120k \Omega$</td>
<td>$0.47nF$</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$200k \Omega$</td>
</tr>
<tr>
<td></td>
<td>$-12.0V &gt; V_{OUTN} \geq -15.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$110k \Omega$</td>
<td>$1.0nF$</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$200k \Omega$</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>$V_{IN}$</th>
<th>Output Voltage Range</th>
<th>$L_{N}$</th>
<th>$C_{LN}$</th>
<th>$R_{ZN}$</th>
<th>$C_{ZN}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>5V±10%</td>
<td>$-4.0V \geq V_{OUTN} \geq -5.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$51k \Omega$</td>
<td>$1.0nF$</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$110k \Omega$</td>
</tr>
<tr>
<td></td>
<td>$-5.0V &gt; V_{OUTN} \geq -9.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$68k \Omega$</td>
<td>$0.47nF$</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$130k \Omega$</td>
</tr>
<tr>
<td></td>
<td>$-9.0V &gt; V_{OUTN} \geq -12.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$120k \Omega$</td>
<td>$0.47nF$</td>
</tr>
<tr>
<td></td>
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<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$200k \Omega$</td>
</tr>
<tr>
<td></td>
<td>$-12.0V &gt; V_{OUTN} \geq -15.0V$</td>
<td>$3.3 \mu H$</td>
<td>$2 \times 4.7 \mu F$</td>
<td>$110k \Omega$</td>
<td>$1.0nF$</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>$2 \times 4.7 \mu F$</td>
<td>$200k \Omega$</td>
</tr>
</tbody>
</table>
XC9519 Series

TYPICAL APPLICATION CIRCUIT
(VIN=3.6V, VOUT+=5.0V, VOUT-=5.0V)

<Typical Examples> VIN=3.6V, VOUT+=5.0V, VOUT-=5.0V

- Capacitor
  - CIN_P : 10μF/10V (C2012JB1A106M, TDK-EPC)
  - CIN_Sw : 4.7μF/10V (C2012JB1A475M, TDK-EPC)
  - CIN_A : 0.1μF/10V (C1005JB1A104K, TDK-EPC)
  - C_lp : 4×4.7μF/10V (C2012JB1A475M, TDK-EPC)
  - C_LN : 4×4.7μF/10V (C2012JB1A475M, TDK-EPC)
  - C_L_VR : 0.22μF/6.3V (C1005JB0J224M, TDK-EPC)
  - C,ZP : 4.7nF/25V (C1005JB1E472K, TDK-EPC)
  - C,ZN : 0.47nF/50V (C1005JB1H471K, TDK-EPC)
  - C,FBP : 47pF/50V (C1005CH1H470J, TDK-EPC)

For CIN_P, CIN_Sw, CIN_A, C_L_VR, CLP, and CLN, use a B characteristic (JIS Standards) or X7R/X5R (EIA Standards), and use a ceramic capacitor with minimal reduction of capacitance when a DC bias is applied.

- Coil, Schottky diode, P-ch MOSFET
  - Lp, Ln : 3.3μH (VLF5014S-3R3M2R0, TDK-EPC)
  - (MSSS121-332, Coilcraft)
  - SBDp, SBDN : XBS304S17R-G (TOREX)
  - CMS03 (TOSHIBA)
  - P-ch MOS : EMH1303 (SANYO)

When selecting external components, refer to the specifications of each component and select so as not to exceed the ratings.

- Resistor
  - RFBP1 : 300kΩ
  - RFBP2 : 75kΩ
  - RFBNZ : 60kΩ
  - RSP : 4.7kΩ
  - RFBN1 : 300kΩ
  - RFBN2 : 60kΩ
  - RZN : 110kΩ
  - RSP : 8.2kΩ
### TYPICAL APPLICATION CIRCUIT (Continued)

\( V_{\text{IN}}=3.6\,\text{V}, V_{\text{OUTP}}=15.0\,\text{V}, V_{\text{OUTN}}=-15.0\,\text{V} \)

For \( C_{\text{INP}}, C_{\text{INSW}}, C_{\text{INN}}, C_{\text{LP}}, C_{\text{LVR}}, \) and \( C_{\text{IN}}, \) use a B characteristic (JIS Standard) or X7R/X5R (EIA Standards), and use a ceramic capacitor with minimal reduction of capacitance when a DC bias is applied.

- Capacitor
  - \( C_{\text{INP}} : 10\,\mu\text{F} / 10\,\text{V} \) (C2012JB1A106M, TDK-EPC)
  - \( C_{\text{INSW}} : 4.7\,\mu\text{F} / 10\,\text{V} \) (C2012JB1A475M, TDK-EPC)
  - \( C_{\text{INN}} : 0.1\,\mu\text{F} / 10\,\text{V} \) (C1005JB1A104K, TDK-EPC)
  - \( C_{\text{LP}} : 4\times4.7\,\mu\text{F} / 25\,\text{V} \) (TKM212BJ475KG, TAIYO YUDEN)
  - \( C_{\text{LVR}} : 0.22\,\mu\text{F} / 6.3\,\text{V} \) (C1005JB0J224M, TDK-EPC)
  - \( C_{\text{L}} : 2.2\,\text{nF} / 50\,\text{V} \) (C1005JB1H222K, TDK-EPC)
  - \( C_{\text{ZP}} : 24\,\text{k}\Omega \) (C1005JB1H471K, TDK-EPC)
  - \( C_{\text{FBP}} : \text{OPEN} \)

- Coil, Schottky diode, P-ch MOSFET
  - \( L_{\text{P}}, L_{\text{N}} : 3.3\,\mu\text{H} \) (VLF5014S-3R3M2R0, TDK-EPC)
  - \( L_{\text{P}}, L_{\text{N}} : 3.3\,\mu\text{H} \) (MSS5121-332, Coilcraft)
  - \( SBD_{\text{P}}, SBD_{\text{N}} : \text{XBS304S17R-G} \) (TOREX)
  - \( SBD_{\text{P}}, SBD_{\text{N}} : \text{CMS03} \) (TOSHIBA)
  - \( P\)-ch MOSFET : EMH1303 \) (SANYO)

When selecting external components, refer to the specifications of each component and select so as not to exceed the ratings.

- Resistor
  - \( R_{\text{FBP1}} : 336\,\text{k}\Omega \)
  - \( R_{\text{FBP2}} : 24\,\text{k}\Omega \)
  - \( R_{\text{FBP3}} : \text{OPEN} \)
  - \( R_{\text{FBN1}} : 360\,\text{k}\Omega \)
  - \( R_{\text{FBN2}} : 24\,\text{k}\Omega \)
  - \( R_{\text{ZP}} : 27\,\text{k}\Omega \)
  - \( R_{\text{ZN}} : 200\,\text{k}\Omega \)

<Typical Examples> \( V_{\text{IN}}=3.6\,\text{V}, V_{\text{OUTP}}=15.0\,\text{V}, V_{\text{OUTN}}=-15.0\,\text{V} \)

- Capacitor
  - \( C_{\text{IN}} \)
  - \( C_{\text{INSW}} \)
  - \( C_{\text{INN}} \)
  - \( C_{\text{LP}} \)
  - \( C_{\text{LVR}} \)
  - \( C_{\text{L}} \)
  - \( C_{\text{ZP}} \)
  - \( C_{\text{ZN}} \)
  - \( C_{\text{FBP}} \)

- Coil, Schottky diode, P-ch MOSFET
  - \( L_{\text{P}}, L_{\text{N}} \)
  - \( SBD_{\text{P}}, SBD_{\text{N}} \)
  - \( P\)-ch MOSFET

When selecting external components, refer to the specifications of each component and select so as not to exceed the ratings.
TEST CIRCUITS

1) Circuit ①

2) Circuit ②

3) Circuit ③

4) Circuit ④
TEST CIRCUITS (Continued)

5) Circuit ⑤

6) Circuit ⑥

7) Circuit ⑦
TEST CIRCUITS (Continued)

8) Circuit ⑧

1. Capacitance between pins
   The capacitances between the following pins are omitted in the circuit diagram.
   - PVIN pin – PGND pin: 1 \( \mu F \)
   - FBP pin - AGND pin: 1 \( \mu F \)
   - FBN pin - AGND pin: 1 \( \mu F \)
   - VREF pin - AGND pin: 1 \( \mu F \)

2. Testing method for on resistance
   Testing is executed at 100% DUTY using test mode.

9) Circuit ⑨
NOTES ON USE

1. For temporary, transitional voltage drop or voltage rising phenomenon, the IC is liable to malfunction should the ratings be exceeded.

2. The characteristics of this IC are highly dependent on peripheral circuits.
   When selecting external components, refer to the specifications of each component and select so as not to exceed the ratings.
   Some peripheral component selections may cause unstable operation.
   Before use, sufficiently test operation using the actual equipment.

3. When the input voltage \( V_{IN} \) is low and the output voltage \( V_{OUTP}/V_{OUTN} \) is high, the input current may be limited by the maximum duty limit and
   the set output voltage may not be output.

4. If the step-up ratio is high and excessive load current flows, the input current may be limited by the maximum duty limit and maximum current
   limit protection and short-circuit protection may not activate.

5. Do not connect a component other than \( C_{L, \text{VR}} \) to the VREF pin.
   If a component other than \( C_{L, \text{VR}} \) is connected, the output voltage \( V_{OUTN} \) of an inverting DC/DC converter may become unstable.

6. For external components, use the components specified in the standard circuit examples and component selection methods.

7. When the input voltage \( V_{IN} \) is high and the output voltage \( V_{OUTP}/V_{OUTN} \) is low, intermittent oscillation may occur during PWM control.

8. If the step-up ratio is low in a step-up DC/DC converter, the output voltage \( V_{OUTP} \) may become unstable during PFM/PWM switching control
   \( (V_{MODE} = "H") \).

9. During PFM/PWM switching control \( (V_{MODE} = "H") \), the output voltage may become unstable near switching between PFM mode and PWM mode.

<External Components>
- \( C_{D} = 4 \times 4.7 \mu F \)
- \( L_{p} = 3.3 \mu H \) (VLF5014S-3R3M2R0)
- SBD:\ CMS03
- P-ch MOS: EMH1303
- \( R_{ZP} = 7.5k \Omega \), \( C_{ZP} = 4.7nF \)

<External Components>
- \( C_{D} = 4 \times 4.7 \mu F \)
- \( L_{p} = 3.3 \mu H \) (VLF5014S-3R3M2R0)
- SBD:\ CMS03
- P-ch MOS: EMH1303
- \( R_{ZP} = 7.5k \Omega \), \( C_{ZP} = 4.7nF \)
10. During PWM control (V\textsubscript{MODE} = "L"), the output voltage may become unstable at light loads.

11. Torex places an importance on improving our products and their reliability.

We request that users incorporate fail-safe designs and post-aging protection treatment when using Torex products in their systems.
NOTES ON USE (Continued)

1. Position external components close to the IC so that the wiring is thick and short.
2. To minimize input voltage fluctuations, place CIN_P and CIN_A as close as possible to the IC.
3. Make the GND wiring sufficiently strong. Fluctuations of AGND or PGND voltage due to GND current during switching may cause unstable IC operation.
4. When creating a layout, refer to the circuit diagram and recommended layout pattern below.
5. This product is incorporated into a driver, and thus the driver transistor current and on-resistance may cause heat generation.

Recommended Pattern Layout

Front

Back side see-through
TYPICAL PERFORMANCE CHARACTERISTICS

(1) Efficiency vs. Output Current

**Step-up DC/DC Converter (V_{OUTP}=5.0V)**

- VIN = 4.4V, 3.6V, 2.7V
- VENP = "H", VENN = "L"
- PWM/PFM (VMODE = "H")
- PWM (VMODE = "L")

**Inverting DC/DC Converter (V_{OUTN}=-5.0V)**

- VIN = 4.4V, 3.6V, 2.7V
- VENP = "L", VENN = "H"
- PWM/PFM (VMODE = "H")
- PWM (VMODE = "L")

(2) Output Voltage vs. Output Current

**Step-up DC/DC Converter (V_{OUTP}=5.0V)**

- VIN = 4.4V, 3.6V, 2.7V
- VENP = "H", VENN = "L"
- PWM/PFM (VMODE = "H")
- PWM (VMODE = "L")

**Inverting DC/DC Converter (V_{OUTN}=-5.0V)**

- VIN = 4.4V, 3.6V, 2.7V
- VENP = "L", VENN = "H"
- PWM/PFM (VMODE = "H")
- PWM (VMODE = "L")

---

**CLP=4×4.7μF, LP=3.3μH (VLF5014S-3R3M2R0)**

- SBDP: CMS03, P-ch MOS: EMH1303, RZP=7.5kΩ, CZP=4.7nF
- Step-up DC/DC Converter (V_{OUTP}=5.0V)

**CLN=4×4.7μF, LN=3.3μH (VLF5014S-3R3M2R0)**

- SBDN: CMS03, RZN=130kΩ, CZN=0.47nF
- Inverting DC/DC Converter (V_{OUTN}=-5.0V)

---

**CLP=4×4.7μF, LP=3.3μH (VLF5014S-3R3M2R0)**

- SBDP: CMS03, P-ch MOS: EMH1303, RZP=27kΩ, CZP=2.2nF
- Step-up DC/DC Converter (V_{OUTP}=15.0V)

**CLN=4×4.7μF, LN=3.3μH (VLF5014S-3R3M2R0)**

- SBDN: CMS03, RZN=200kΩ, CZN=0.47nF
- Inverting DC/DC Converter (V_{OUTN}=-15.0V)
(2) Output Voltage vs. Output Current (Continued)

### Step-up DC/DC Converter ($V_{OUTP} = 15.0V$)

- $C_{LP} = 4.7 \mu F, L_{P} = 3.3 \mu H$ (VLF5014S-3R3M2R0)
- SBDP: CMS03, P-ch MOS: EMH1303, $R_{ZP} = 27k \Omega$, $C_{ZP} = 2.2nF$

### Inverting DC/DC Converter ($V_{OUTN} = -15.0V$)

- $C_{LP} = 4.7 \mu F, L_{P} = 3.3 \mu H$ (VLF5014S-3R3M2R0)
- SBDN: CMS03, R$_{ZN} = 200k \Omega$, C$_{ZN} = 0.47nF$

### Step-up DC/DC Converter ($V_{OUTP} = 5.0V$)

- $C_{LP} = 4.7 \mu F, L_{P} = 3.3 \mu H$ (VLF5014S-3R3M2R0)
- SBDP: CMS03, P-ch MOS: EMH1303, $R_{ZP} = 7.5k \Omega$, $C_{ZP} = 4.7nF$

### Inverting DC/DC Converter ($V_{OUTN} = -5.0V$)

- $C_{LP} = 4.7 \mu F, L_{P} = 3.3 \mu H$ (VLF5014S-3R3M2R0)
- SBDN: CMS03, $R_{ZN} = 130k \Omega$, $C_{ZN} = 0.47nF$

### Step-up DC/DC Converter ($V_{OUTP} = 15.0V$)

- $C_{LP} = 4.7 \mu F, L_{P} = 3.3 \mu H$ (VLF5014S-3R3M2R0)
- SBDP: CMS03, P-ch MOS: EMH1303, $R_{ZP} = 27k \Omega$, $C_{ZP} = 2.2nF$

### Inverting DC/DC Converter ($V_{OUTN} = -15.0V$)

- $C_{LP} = 4.7 \mu F, L_{P} = 3.3 \mu H$ (VLF5014S-3R3M2R0)
- SBDN: CMS03, $R_{ZN} = 200k \Omega$, $C_{ZN} = 0.47nF$
XC9519 Series

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(4) Oscillation Frequency vs. Ambient Temperature

(5) Supply Current 1, 2, 3 vs. Ambient Temperature

Supply Current 1

Supply Current 2

Supply Current 3
# TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(6) FBP Voltage vs. Ambient Temperature

(7) Output Voltage Accuracy vs. Ambient Temperature

(8) UVLO Voltage vs. Ambient Temperature

(9) ENP "H" Voltage vs. Ambient Temperature

(10) ENP "L" Voltage vs. Ambient Temperature
TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

(11) ENN "H" Voltage vs. Ambient Temperature

(12) ENN "L" Voltage vs. Ambient Temperature

(13) MODE "H" Voltage vs. Ambient Temperature

(14) MODE "L" Voltage vs. Ambient Temperature

(15) LXP SW "H" ON Resistance vs. Ambient Temperature

(16) LXN SW "L" ON Resistance vs. Ambient Temperature
(17) Output Voltage Rise Wave Form

Step-up DC/DC Converter ($V_{OUTP}$=15.0V)
- $C_L=4.7 \mu F$, $L_O=3.3 \mu H$ (VLF5014S-3R3M2R0)
- SBD: CMS03, P-ch MOS: EMH1303, $R_{DP}=27k \Omega$, $C_D=2.2nF$

Inverting DC/DC Converter ($V_{OUTN}$=-15.0V)
- $C_L=4.7 \mu F$, $L_O=3.3 \mu H$ (VLF5014S-3R3M2R0)
- SBD: CMS03, $R_{DP}=200k \Omega$, $C_D=0.47nF$

(18) Soft Start Time vs. Ambient Temperature

Step-up DC/DC Converter
- $V_{DD}=3.6V$, $V_{IN}=3.6V$, $V_{OUT}=5.5V$
- $V_{DD}=3.6V$, $V_{IN}=3.6V$, $V_{OUT}=5.5V$

Inverting DC/DC Converter
- $V_{DD}=3.6V$, $V_{IN}=3.6V$, $V_{OUT}=-5.5V$
- $V_{DD}=3.6V$, $V_{IN}=3.6V$, $V_{OUT}=-5.5V$

(19) Maximum Duty Cycle vs. Ambient Temperature

Step-up DC/DC Converter
- $V_{DD}=3.6V$, $V_{IN}=3.6V$, $V_{OUT}=5.5V$
- $V_{DD}=3.6V$, $V_{IN}=3.6V$, $V_{OUT}=-5.5V$

Inverting DC/DC Converter
- $V_{DD}=3.6V$, $V_{IN}=3.6V$, $V_{OUT}=5.5V$
- $V_{DD}=3.6V$, $V_{IN}=3.6V$, $V_{OUT}=-5.5V$
(20) Maximum Current Limit vs. Ambient Temperature

Step-up DC/DC Converter (V_{OUT}=5.0V)

\[ C_{L}=4 \times 4.7 \mu F, L_{P}=3.3 \mu H \] (VLF5014S-3R3M2R0)

SBDp: CMS03, P-ch MOS: EMH1303, R_{ZP}=7.5k\Omega, C_{ZP}=4.7nF

Inverting DC/DC Converter (V_{OUT}=-5.0V)

\[ C_{L}=4 \times 4.7 \mu F, L_{P}=3.3 \mu H \] (VLF5014S-3R3M2R0)

SBDn: CMS03, R_{ZN}=130k\Omega, C_{ZN}=0.47nF

(21) Latch Time vs. Ambient Temperature
(22) CL Discharge Resistance vs. Ambient Temperature

**Step-up DC/DC Converter**

- **Equation:**
  \[ C_{\text{discharge}} = \frac{V_{\text{OUTP}}}{R_{\text{DCHGP}}} \]
- **Conditions:**
  \[ V_{\text{IN}} = 5.5 \text{V}, V_{\text{ENP}} = \text{H}, V_{\text{ENN}} = \text{L}, V_{\text{MODE}} = \text{H} \]
- **Components:**
  - CLP = 4 × 4.7 μF, LP = 3.3 μH (VLF5014S-3R3M2R0)
  - SBDP: CMS03, P-ch MOS: EMH1303, RZP = 7.5 kΩ, CZP = 4.7 nF

**Inverting DC/DC Converter**

- **Equation:**
  \[ C_{\text{discharge}} = \frac{V_{\text{OUTN}}}{R_{\text{DCHGN}}} \]
- **Conditions:**
  \[ V_{\text{IN}} = 5.5 \text{V}, V_{\text{ENP}} = \text{H}, V_{\text{ENN}} = \text{L}, V_{\text{MODE}} = \text{H} \]
- **Components:**
  - CLN = 4 × 4.7 μF, LN = 3.3 μH (VLF5014S-3R3M2R0)
  - SBDN: CMS03, RZN = 130 kΩ, CZN = 0.47 nF

(23) PFM Switching Current vs. Ambient Temperature

**Step-up DC/DC Converter (V\text{OUTP}=5.0V)**

- **Equation:**
  \[ I_{\text{PFMP}} = \frac{V_{\text{OUTP}}}{R_{\text{PFMP}}} \]
- **Conditions:**
  \[ V_{\text{IN}} = 4.4 \text{V}, V_{\text{ENP}} = \text{H}, V_{\text{ENN}} = \text{L}, V_{\text{MODE}} = \text{H} \]
- **Components:**
  - CLP = 4 × 4.7 μF, LP = 3.3 μH (VLF5014S-3R3M2R0)
  - SBDP: CMS03, P-ch MOS: EMH1303, RZP = 7.5 kΩ, CZP = 4.7 nF

**Inverting DC/DC Converter (V\text{OUTN}=5.0V)**

- **Equation:**
  \[ I_{\text{PFMN}} = \frac{V_{\text{OUTN}}}{R_{\text{PFMN}}} \]
- **Conditions:**
  \[ V_{\text{IN}} = 4.4 \text{V}, V_{\text{ENP}} = \text{L}, V_{\text{ENN}} = \text{H}, V_{\text{MODE}} = \text{H} \]
- **Components:**
  - CLN = 4 × 4.7 μF, LN = 3.3 μH (VLF5014S-3R3M2R0)
  - SBDN: CMS03, RZN = 200 kΩ, CZN = 0.47 nF

**Step-up DC/DC Converter (V\text{OUTP}=15.0V)**

- **Equation:**
  \[ I_{\text{PFMP}} = \frac{V_{\text{OUTP}}}{R_{\text{PFMP}}} \]
- **Conditions:**
  \[ V_{\text{IN}} = 5.5 \text{V}, V_{\text{ENP}} = \text{H}, V_{\text{ENN}} = \text{L}, V_{\text{MODE}} = \text{H} \]
- **Components:**
  - CLP = 4 × 4.7 μF, LP = 3.3 μH (VLF5014S-3R3M2R0)
  - SBDP: CMS03, P-ch MOS: EMH1303, RZP = 27 kΩ, CZP = 2.2 nF

**Inverting DC/DC Converter (V\text{OUTN}=15.0V)**

- **Equation:**
  \[ I_{\text{PFMN}} = \frac{V_{\text{OUTN}}}{R_{\text{PFMN}}} \]
- **Conditions:**
  \[ V_{\text{IN}} = 5.5 \text{V}, V_{\text{ENP}} = \text{L}, V_{\text{ENN}} = \text{H}, V_{\text{MODE}} = \text{H} \]
- **Components:**
  - CLN = 4 × 4.7 μF, LN = 3.3 μH (VLF5014S-3R3M2R0)
  - SBDN: CMS03, RZN = 200 kΩ, CZN = 0.47 nF
(24) Load Transient Response

Step-up DC/DC Converter: PWM/PFM MODE  
\( (V_{IN}=3.6\text{V}, V_{OUTP}=5.0\text{V}, I_{OUTP}=1\Rightarrow200\text{mA}) \)  
\( C_{LP}=4\times4.7\mu\text{F}, L_{P}=3.3\mu\text{H} \) \( \text{VLF5014S-3R3M2R0}, \text{SBDO: CMS03} \)  
P-ch MOS: EMH1303, \( R_{ZP}=8.2\Omega, C_{DP}=4.7\text{nF}, C_{FBP}=47\text{pF}, R_{SP}=4.7\text{k}\Omega \)

Step-up DC/DC Converter: PWM MODE  
\( (V_{IN}=3.6\text{V}, V_{OUTP}=5.0\text{V}, I_{OUTP}=200\Rightarrow1\text{mA}) \)  
\( C_{LP}=4\times4.7\mu\text{F}, L_{P}=3.3\mu\text{H} \) \( \text{VLF5014S-3R3M2R0}, \text{SBDO: CMS03} \)  
P-ch MOS: EMH1303, \( R_{ZP}=8.2\Omega, C_{DP}=4.7\text{nF}, C_{FBP}=47\text{pF}, R_{SP}=4.7\text{k}\Omega \)
TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

Step-up DC/DC Converter: PWM MODE
\( (V_{IN}=3.6\text{V}, V_{OUTP}=15.0\text{V}, I_{OUTP}=1\Rightarrow50\text{mA}) \)

\( C_L=4\times4.7\mu\text{F}, L_p=3.3\mu\text{H} \) (VLF5014S-3R3M2R0)

SBD: CMS03, P-ch MOS: EMH1303, \( R_{OP}=27k\Omega \), \( C_{OP}=2.2\text{nF} \)

Step-up DC/DC Converter: PWM MODE
\( (V_{IN}=3.6\text{V}, V_{OUTP}=15.0\text{V}, I_{OUTP}=50\Rightarrow1\text{mA}) \)

\( C_L=4\times4.7\mu\text{F}, L_p=3.3\mu\text{H} \) (VLF5014S-3R3M2R0)

SBD: CMS03, P-ch MOS: EMH1303, \( R_{OP}=27k\Omega \), \( C_{OP}=2.2\text{nF} \)

Step-up DC/DC Converter: PWM/PFM MODE
\( (V_{IN}=3.6\text{V}, V_{OUTP}=15.0\text{V}, I_{OUTP}=1\Rightarrow50\text{mA}) \)

\( C_L=4\times4.7\mu\text{F}, L_p=3.3\mu\text{H} \) (VLF5014S-3R3M2R0)

SBD: CMS03, P-ch MOS: EMH1303, \( R_{OP}=27k\Omega \), \( C_{OP}=2.2\text{nF} \)

Step-up DC/DC Converter: PWM/PFM MODE
\( (V_{IN}=3.6\text{V}, V_{OUTP}=15.0\text{V}, I_{OUTP}=50\Rightarrow1\text{mA}) \)

\( C_L=4\times4.7\mu\text{F}, L_p=3.3\mu\text{H} \) (VLF5014S-3R3M2R0)

SBD: CMS03, P-ch MOS: EMH1303, \( R_{OP}=27k\Omega \), \( C_{OP}=2.2\text{nF} \)
Inverting DC/DC Converter: PWM MODE
(VIN=3.6V, VOUTN=-5.0V, IOUTN=1⇒200mA)
C_L=4×4.7μF, L_N=3.3μH (VLF5014S-3R3M2R0)
SBD_N: CMS03, R_ZN=130kΩ, C_ZN=0.47nF

Inverting DC/DC Converter: PWM MODE
(VIN=3.6V, VOUTN=-5.0V, IOUTN=200⇒1mA)
C_L=4×4.7μF, L_N=3.3μH (VLF5014S-3R3M2R0)
SBD_N: CMS03, R_ZN=130kΩ, C_ZN=0.47nF

Inverting DC/DC Converter: PWM/PFM MODE
(VIN=3.6V, VOUTN=-5.0V, IOUTN=1⇒200mA)
C_L=4×4.7μF, L_N=3.3μH (VLF5014S-3R3M2R0)
SBD_N: CMS03, R_ZN=130kΩ, C_ZN=0.47nF

Inverting DC/DC Converter: PWM/PFM MODE
(VIN=3.6V, VOUTN=-5.0V, IOUTN=200⇒1mA)
C_L=4×4.7μF, L_N=3.3μH (VLF5014S-3R3M2R0)
SBD_N: CMS03, R_ZN=130kΩ, C_ZN=0.47nF
■ TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

Inverting DC/DC Converter: PWM MODE
\( V_{IN}=3.6V, V_{OUTN}=-15.0V, I_{OUTN}=1\Rightarrow50mA \)
\( C_{IN}=4\times4.7\mu F, L_{N}=3.3\mu H \) (VLF5014S-3R3M2R0)

SBD\(_{DN}\): CMS03, \( R_{ZN}=200k\Omega, C_{ZN}=0.47nF \)

Inverting DC/DC Converter: PWM/PFM MODE
\( V_{IN}=3.6V, V_{OUTN}=-15.0V, I_{OUTN}=1\Rightarrow50mA \)
\( C_{IN}=4\times4.7\mu F, L_{N}=3.3\mu H \) (VLF5014S-3R3M2R0)

SBD\(_{DN}\): CMS03, \( R_{ZN}=200k\Omega, C_{ZN}=0.47nF \)

\( V_{OUT}=\text{L}, V_{IN}=\text{L}, V_{ODE}=\text{L} \)

\( I_{OUT} = 1\Rightarrow50mA \)

\( 1ch: 500mV/div, 2ch: 50mA/div \)

\( \text{time: } 100\mu s/\text{div} \)

\( V_{OUT} \)

\( 1ch \)

\( 2ch \)

\( I_{OUT} \)

\( 1ch: 500mV/div, 2ch: 50mA/div \)

\( \text{time: } 500\mu s/\text{div} \)

\( V_{OUT} \)

\( 1ch \)

\( 2ch \)

\( I_{OUT} = 50\Rightarrow1mA \)
XC9519 Series

■ PACKAGING INFORMATION

● QFN-24 (unit:mm)

![QFN-24 Pinout Diagram]

● QFN-24 Reference Pattern Layout (unit:mm)

![QFN-24 Reference Pattern Layout Diagram]

● QFN-24 Reference Metal Mask Design (unit:mm)

![QFN-24 Reference Metal Mask Design Diagram]
### MARKING RULE

1pin QFN-24

#### ① represent product series.

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<thead>
<tr>
<th>MARK</th>
<th>PRODUCT SERIES</th>
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<tr>
<td>9</td>
<td>XC9519******.G</td>
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</tbody>
</table>

#### ② represents UVLO detect voltage.

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<th>UVLO VOLTAGE</th>
<th>PRODUCT SERIES</th>
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<tr>
<td>A</td>
<td>Detect: 2.2V, Hysteresis Width: 0.2V</td>
<td>XC9519A******-G</td>
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#### ③④ represents oscillation frequency and maximum current limit.

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<th>OSCILLATION FREQUENCY</th>
<th>MAXIMUM CURRENT LIMIT</th>
<th>PRODUCT SERIES</th>
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<td>2</td>
<td>1.2MHz</td>
<td>2.0A</td>
</tr>
</tbody>
</table>

#### ⑤⑥ represents production lot number.

- 01～09, 0A～0Z, 11～9Z, A1～A9, AA～AZ, and B1～ZZ repeated
- (G, I, J, O, Q, W excluded)
- *No character inversion used.*
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